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*Botti, E.; Mandrini, T.; Stefani, F.;*

Solid-State Circuits, IEEE Journal of , Volume: 31 , Issue: 12 , Dec. 1996

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**2 High slew rate monolithic operational amplifier using compatible complementary P-N-P's**
*Davis, P.C.; Moyer, S.F.; Saari, V.R.;*

Solid-State Circuits, IEEE Journal of , Volume: 9 , Issue: 6 , Dec 1974

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**3 A low-voltage CMOS OTA with rail-to-rail differential input range**
*Li, M.F.; Dasgupta, U.; Zhang, X.W.; Yong Ching Lim;*

Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on [see also Circuits and Systems I: Regular Papers, IEEE Transactions on] , Volume: 47 , Issue: 1 , Jan. 2000

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**4 Design, analysis, and implementation of analog complex filter for low Wireless LAN application**
*Teo, T.H.; Khoo, E.-S.; Uday, D.; Tear, C.-B.;*

VLSI Design, 2004. Proceedings. 17th International Conference on , 2004

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